

IN THE SPECIFICATION:

Please replace the paragraph at line 2 of page 2 with the following amended paragraph:

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β1

- - For data processing transmitted data, conventional communications processors which can be used in a flexible way are employed. Figure 2 shows a conventional communications processor 35 according to the prior art. The data signals which are output by a data source DQ 36 are converted into digital signals by an analog/digital converter 37 and fed to a data input E of the communications processor 35. The incoming digital data stream  $d_{in}$  is fed into an input buffer or an input port 38. This input port 38 can be addressed via an address bus 39 of the communications processor 35. The conventional processor 35 which is illustrated in figure 2 has an address bus 39 for addressing the various interface memories or ports 38 and 40 and the data memory 41. The data memory 41 is composed here, e.g. of a plurality of RAM memories 42. The data memory 41 and the interface buffers or ports 38 and 40 are connected to a common data bus 43 for internally exchanging data within the communications processor 35. The core of the processor 35 is formed by a data processing unit 44 which contains internal registers R. The data processing unit 44 is connected to a read only memory or ROM memory 45 in which the executable program is stored. The output ports or output interface memories 38 and 40 are connected via data outputs A for outputting an output data stream  $d_{out}$  to digital/analog converters 46 which convert the output digital data into analog output signals. The analog output signals are output to a data sink 47, for example a terminal, via analog lines. - -

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